

1. All on-board RAM REMOVED
2.  $\overline{F0-F7}$  decode line taken off board thru E19 to DISK I/O Board.
3. SYMDOS IN EPROM ~~2716~~<sup>2532</sup> at U23 mapped at \$9000
4. BASIC Pissy backed IN ~~2716's~~<sup>2532</sup> at U22 U21
5. RAE IN ~~2716's~~<sup>2532</sup> at U22 U21
6. Debug LED added per appendix H-2
7. Buffer B7 on PB7 of 6522 @ \$A000 is wired as an RS232 Input buffer like Q29 on serial port. Input to this buffer is jumpered to Tconnector pin 4.
8. U27 pin 25 is wired to U5 pin 4 (Allows IRQ by on board 6532 chip)
9. Trace cut on U1 between pin 4 and 5 on bottom of board. Jumper U1-4 to U2-5. Takes  $\overline{\Phi_2}$  to 74LS138 decoder as recommended by synertek bulletin.
10. Jumper plug installed to allow program control of Debug
11. External Reset button INSTALLED
12. Removed jumper GG-39. Installed jumper GG to 40 (makes terminal use +5 to -12 volt level)